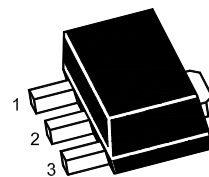




PNP Silicon Epitaxial Planar Transistor

for high current application



1.Base 2.Collector 3.Emitter

SOT-89-3L

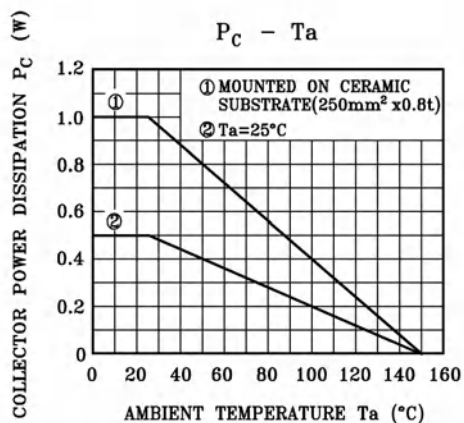
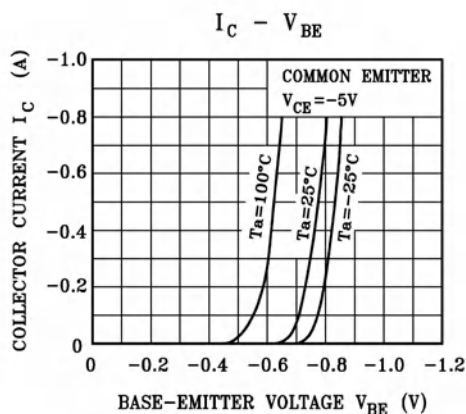
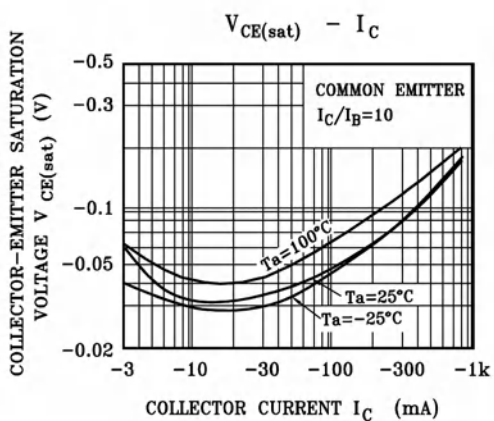
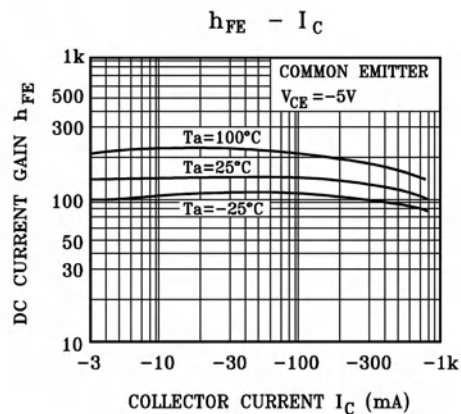
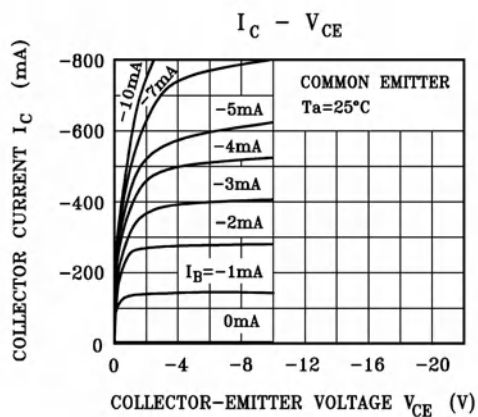
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Collector Base Voltage	$-V_{CBO}$	120	V
Collector Emitter Voltage	$-V_{CEO}$	120	V
Emitter Base Voltage	$-V_{EBO}$	5	V
Collector Current	$-I_C$	800	mA
Base Current	$-I_B$	160	mA
Total Power Dissipation	P_{tot}	0.5 1 ¹⁾	W
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

¹⁾ When mounted on a 250 mm² X 0.8 t ceramic substrate.

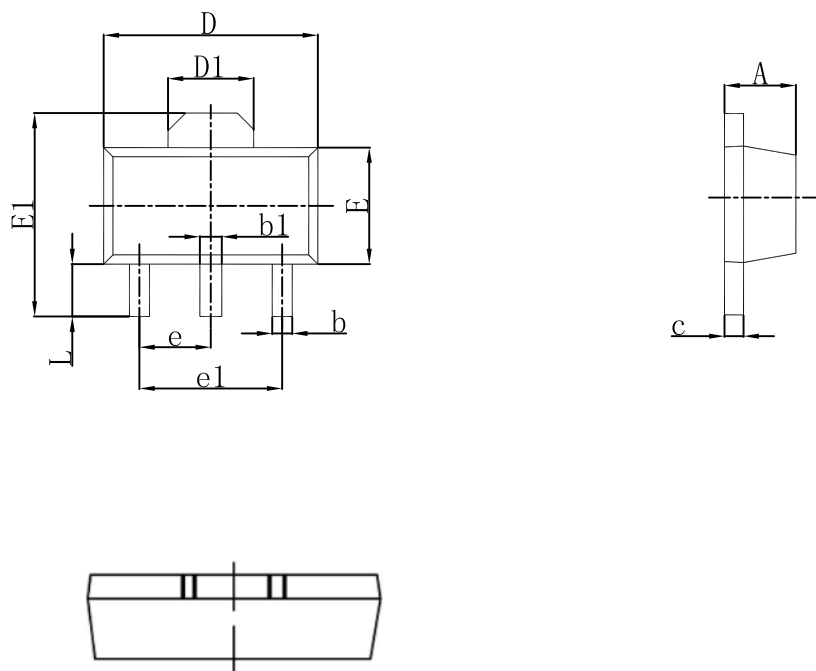
Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $-V_{CE} = 5\text{ V}$, $-I_C = 100\text{ mA}$ Current Gain Group O Y	h_{FE}	80	-	160	-
	h_{FE}	120	-	240	-
Collector Base Cutoff Current at $-V_{CB} = 120\text{ V}$	$-I_{CBO}$	-	-	100	nA
Emitter Base Cutoff Current at $-V_{EB} = 5\text{ V}$	$-I_{EBO}$	-	-	100	nA
Collector Emitter Breakdown Voltage at $-I_C = 10\text{ mA}$	$-V_{(BR)CEO}$	120	-	-	V
Emitter Base Breakdown Voltage at $-I_E = 1\text{ mA}$	$-V_{(BR)EBO}$	5	-	-	V
Collector Emitter Saturation Voltage at $-I_C = 500\text{ mA}$, $-I_B = 50\text{ mA}$	$-V_{CE(sat)}$	-	-	1	V
Base Emitter on Voltage at $-V_{CE} = 5\text{ V}$, $-I_C = 500\text{ mA}$	$-V_{BE(on)}$	-	-	1	V
Transition Frequency at $-V_{CE} = 5\text{ V}$, $-I_C = 100\text{ mA}$	f_T	-	120	-	MHz
Collector Output Capacitance at $-V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$	C_{ob}	-	-	30	pF





SOT-89-3L Outlines Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047